

GUI BASED VERIFICATION USING VC++ AND VERILOG PLI

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Abstract

PLI interface is widely used with Verilog to create test benches that are easier to code, more efficient and re-usable. This paper emphasizes the use of C++ rather than C so that the object oriented methodology and the C++ standard template library can be used. It then goes one step further by explaining the use of GUI based on Microsoft Foundation Classes (MFC) library using Visual C++. This also allows us to use powerful debugging features of visual studio. A tutorial with sample example in Windows/ Modelsim environment is presented, which can be extended to other environments.

Keywords: Verilog PLI Visual C++ verification VLSI