

CRITICAL ANALYSES OF PROCESS SYNCHRONIZATION @ AN ENGINEERING EXAMINATION ENVIRONMENT

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Abstract

This attempt to measure and monitor synchronous parameters is an extension of the technical paper submitted by us titled 'SYNCHRONOUS MANUFACTURING- A RADICAL ANALYSIS TOOL FOR AN INDUSTRIAL ENGINEER' in the APIEMS 2006 Conference held at Bangkok. We now discuss our attempts to apply the same for an Engineering Examination process that an institution undergoes every semester. The process is complicated and is tightly scheduled with no extra time left either for staff or students. This process is highly coordinated and is well established in many universities over many decades across India. The scenario calls for critical analyses of synchronous parameters as they are well connected and no time is provided for any delays. The situation becomes uncontrollable if large delays exist. We have analyzed the need, role and delays leading to synchronicity failing which it develop cascading effects. Hence an attempt to measure synchronicity is required and well developed thoughts are useful in such engineering processes.

Keywords: Synchronicity, Evaluation Process, Delay Analyses, Repetitive Processes, Modeling, and Examination schedules.